



DSD 2018

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21st Euromicro Conference on Digital System Design
Prague, Czech Republic, Aug. 29th – Aug. 31st, 2018

Call for Papers



SCOPE

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded and high-performance) digital and mixed HW/SW system engineering, covering the whole design trajectory from specification and system-level down to micro-architectures, digital circuits and VLSI implementations. It is a forum for researchers and engineers from academia and industry working on advanced investigations, developments and applications.

It focuses on today's and future challenges of advanced embedded, high-performance and cyber-physical applications; system and processor architectures for embedded and high-performance HW/SW systems; design methodology and design automation for all design levels of embedded, high-performance and cyber-physical systems; modern implementation technologies from full custom in nanometer technology nodes, through FPGAs, to MPSoC infrastructures.

Authors are kindly invited to submit their work according (but not limited) to the main topics of the conference main track. In addition, several Special Sessions (with their own coordinators and subprogram committees) do also welcome contributions in specific themes of particular interest. All papers are reviewed following guidelines, quality requirements and thresholds that are common to all committees.

Extended versions of selected best papers will be published in a special issue of the ISI indexed Euromicro/Elsevier journal "Microprocessors and Microsystems: Embedded Hardware Design" (MICPRO) having the 2016 Impact Factor as high as 1.025.

SUBMISSION GUIDELINES

Authors are encouraged to submit their manuscripts via EasyChair web service at web page <https://easychair.org/conferences/?conf=dsd2018>. Should an unexpected web access problem be encountered, please contact the Program Chair by email (dsd2018@easychair.org).

Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, no indication of the authors' names should appear in the manuscript, references included.

IEEE Conference Publishing Services (CPS) will publish accepted papers in the conference proceedings and the proceedings will be submitted to the IEEE Xplore Digital library and indexing services.

Extended versions of selected best papers will be published in a special issue of the ISI indexed Euromicro/Elsevier journal "Microprocessors and Microsystems: Embedded Hardware Design" (MICPRO) having the 2016 Impact Factor as high as 1.025.

IMPORTANT DATES

Deadline for paper submission: **April 19th**
Notification of acceptance: **June 1st**
Camera ready papers: **June 21st**

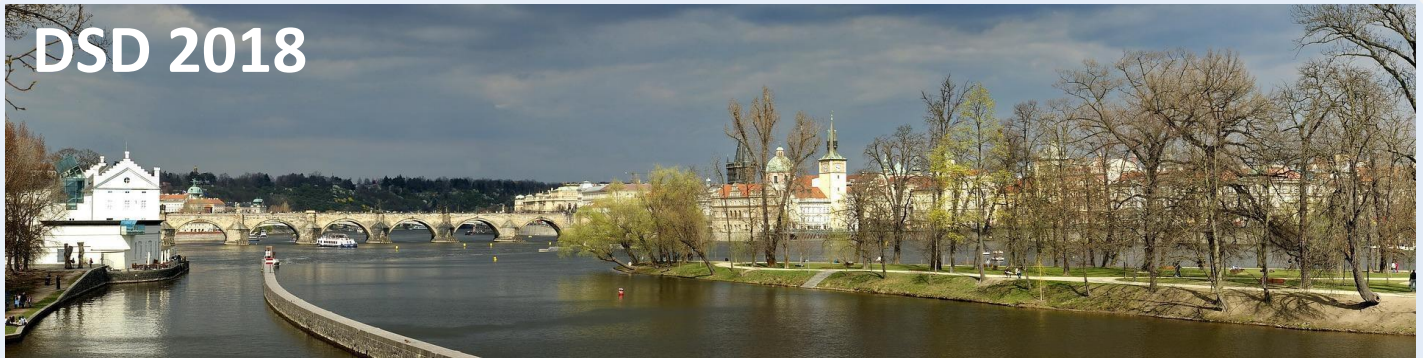
MORE INFORMATION (WEB PAGES)

- DSD 2018: <http://dsd-seaa2018.fit.cvut.cz/dsd/>
- Euromicro: www.euromicro.org



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MAIN TOPICS

- Design of systems, hardware and embedded software
- Specification, modeling, verification and test for systems, hardware and embedded software
- System and processor architecture, high-level and logic synthesis
- High-performance and/or low-energy architectures, circuits and systems
- Systems-on-a-chip, networks-on-a-chip, and memory and communication architectures
- Programmable/re-configurable/adaptable architectures and systems
- Formal methods in system, hardware and embedded software design
- Defect-free/fault-tolerant/attack-resistant/safe/secure design
- Advanced applications of embedded and cyber-physical systems
- Application analysis and parallelization for embedded and high-performance design
- New issues introduced by emerging applications and technologies

SPECIAL SESSIONS/ORGANIZERS

- **AMTDL:** Applications, Architectures, Methods and Tools for Deep-Learning and AI
H. Corporaal (TU/e Eindhoven, NL), M. Srbek (CTU Prague, CZ)
- **AHSA:** Architectures and Hardware for Security Applications
P. Kitsos (TEI of Western Greece, GR), G. Pelosi (Politecnico di Milano, IT)
- **ASAASIT:** Architectures and Systems for Automotive, Aeronautic, Space and Intelligent Transportation
S. Niar (Université de Valenciennes, FR), H. R. Pourshaghghi (Eindhoven University of Technology, NL), I. Alouani (Université de Valenciennes, FR)
- **ASHWPA:** Advanced Systems in Healthcare, Wellness and Personal Assistance
F. Loporati (U Pavia, IT)
- **DCPS:** Design of Cyber-Physical Systems
D. Goswami (TU/e Eindhoven, NL), A. Hamann (Bosch, DE)
- **DTFT:** Dependability, Testing and Fault Tolerance in Digital Systems
P. Fiser (CTU Prague, CZ), Z. Kotasek (TU Brno, CZ)
- **EPDSD:** European Projects in Digital System Design
F. Loporati (U Pavia, IT), L. Jozwiak (TUE, NL)
- **FTET:** Future Trends in Emerging Technologies
O. Keszocze (U Bremen, DE), P. Fiser (CTU Prague, CZ)
- **MCS DIA:** Mixed-Criticality System Design, Implementation and Analysis
K. Grüttner (OFFIS, DE)
- **SDCIS:** System Design for Collaborating Intelligent Systems
E. Ebeid (U Southern Denmark, DK), R. Jacobsen (Aarhus U, DK), B. Beach (U Southern Denmark, DK)

The author's session selection may be changed to create a thematically comprehensive DSD conference program.