



DSD 2018

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21st Euromicro Conference on Digital System Design

Prague, Czech Republic, Aug. 29th – Aug. 31st, 2018

Preliminary Call for Papers



SCOPE

The Euromicro Conference on Digital System Design (DSD) addresses all aspects of (embedded, pervasive and high-performance) digital and mixed HW/SW system engineering, covering the whole design trajectory from specification down to micro-architectures, digital circuits and VLSI implementations. It is a forum for researchers and engineers from academia and industry working on advanced investigations, developments and applications.

It focuses on today's and future challenges of advanced embedded, high-performance and cyber-physical applications; system and processor architectures for embedded and high-performance HW/SW systems; design methodology and design automation for all design levels of embedded, high-performance and cyber-physical systems; modern implementation technologies from full custom in nanometer technology nodes, through FPGAs, to MPSoC infrastructures.

Authors are kindly invited to submit their work according (but not limited) to the main topics of the conference main track. In addition, several Special Sessions (with their own coordinators and subprogram committees) do also welcome contributions in specific themes of particular interest (for details see <http://dsd-seaa2017.ocg.at/dsd2017/>). All papers are reviewed following guidelines, quality requirements and thresholds that are common to all committees.

Extended versions of selected best papers will be published in a special issue of the ISI indexed Euromicro/Elsevier journal "Microprocessors and Microsystems: Embedded Hardware Design" (MICPRO) having the 2016 Impact Factor as high as 1.025.

CALL FOR SPECIAL SESSIONS PROPOSALS

The DSD conference focus on today's and future challenges is reflected also in the structure of Special Sessions. As the digital design world changes, the structure of Special Sessions dynamically changes as well – new Special Sessions are created, while older topics are suspended. For example, between 2011 and 2017, eight new sessions have seen the light of the day and two sessions joined together, while seven sessions were replaced. In DSD 2018 one of the new special sessions will be on **"Applications, architectures, methods and tools for Deep-Learning and AI"**.

The Steering Committee of the Euromicro DSD conference welcomes **proposals for Special Sessions on relevant novel, emerging and hot topics in the field of digital and cyber-physical system design**. We kindly ask the researchers who are willing to organize the special sessions on cutting-edge topics, to **send** their proposals to the Chairman of the Steering Committee, **prof. Lech Jozwiak, L.Jozwiak@tue.nl, before September 30th, 2017**.

Thank you!

SUBMISSION GUIDELINES

Submissions via EasyChair system will be open during fall of 2017. Submission guidelines will be published at web pages of DSD 2018 conference: <http://dsd-seaa2018.fit.cvut.cz/dsd/>

Each manuscript should include the complete paper text, all illustrations, and references. The manuscript should conform to the IEEE format: single-spaced, double column, US letter page size, 10-point size Times Roman font, up to 8 pages. In order to conduct a blind review, **no indication of the authors' names should appear in the manuscript, references included**.

IEEE Conference Publishing Services (CPS) will publish accepted papers in the conference proceedings and the proceedings will be submitted to the IEEE Xplore Digital library and indexing services.

Extended versions of selected best papers will be published in a special issue of the ISI indexed Euromicro/Elsevier journal "Microprocessors and Microsystems: Embedded Hardware Design" (MICPRO) having the 2016 Impact Factor as high as 1.025.

IMPORTANT DATES

Deadline for paper submission: April 1st
Notification of acceptance: May 15th
Camera ready papers: June 15th

MORE INFORMATION (WEB PAGES)

- DSD 2018: <http://dsd-seaa2018.fit.cvut.cz/dsd/>
- Euromicro: www.euromicro.org



21st Euromicro Conference on Digital System Design

Prague, Czech Republic, Aug. 29th – Aug. 31st, 2018

Conference Venue & Czech Republic

Prague, the capital of the Czech Republic, is located in the heart of Europe. It is easily accessible by plane, train, car or bus. Prague airport offers direct connections to around 130 destinations in Europe, Asia, Africa and North America.



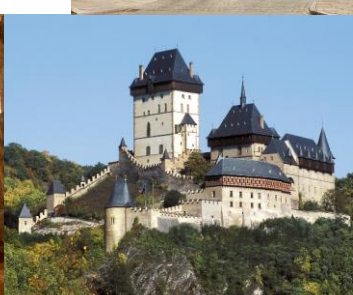
Historical center of Prague, that includes Prague Castle, Lesser Quarter, Charles Bridge, Old Town with mysterious Jewish Town, and New Town, is listed as UNESCO world heritage since 1992.

Historical center of Kutná Hora (located about 70 km to the east of Prague) with medieval silver mines, st. Barbara church, and part of Sedlec suburb where ossuary is located, are listed as UNESCO world heritage since 1995.



Český Krumlov in south Bohemia is famous for its historical center on the meanders of Vltava river, and the second largest castle with fabulous park. It is listed as UNESCO world heritage since 1992.

Czech Republic is renowned for its beer that is brewed in several large and many local small breweries. Did you know that "Pils" type of beer is named after Pilsner Urquell? It is the world's first-ever pilsner type blond lager, created in Pilsen on 5th October 1842.



Close to the south-west border of Prague you find marvelous nature surrounding Berounka river. Koněpruské limestone caves, Karlštejn and Křivoklát castles, and Nižbor glassworks are most frequented points of interest.